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Manfred Beck

Date: May 13, 2005

2829
J.C.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/090,289 Confirmation No: 6619
Applicant : Johann Winderl
Filed : March 4, 2002
Title : Electric Component with Stacked Semiconductor Chips
Art Unit : 2829
Examiner : Luan C. Thai
Docket No. : MAS-FIN-153
Customer No. : 24131

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(i)

Hon. Commissioner for Patents

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

United States patent No. 6,191,370 B1 (Oh), dated February 20, 2001;

German Examination Report dated December 16, 2004.

It is believed that the enclosed prior art is less pertinent than the prior art previously submitted and cited by the Examiner. Kindly place the references in the Patent Office file wrapper.

Respectfully submitted,

Manfred Beck

For Applicant
Date: May 13, 2005
Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101
/bmb

MANFRED BECK
REG. NO. 45,342

FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEINFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))

MAY 17 2005

Attorney Docket No.:

MAS-FIN-153

Applic. No.

10/090,289

Applicant

Johann Winderl

Filing Date

March 4, 2002

Group Art Unit

2829

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	6,191,370 B1	02/20/01	Oh			
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENT

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES	NO
	J							
	K							
	L							
	M							
	N							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	O	
	P	

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.